

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Kourosh GHARACHORLOO et al.	\$	Anticipated Group Art Unit:	2187
Serial No.:	Not Yet Assigned	\$	Anticipated Examiner:	K. Takeguchi
Filed:	Concurrently Herewith	\$		
For:	System And Method For Limited Fanout Daisy Chaining Of Cache Invalidation Requests In A Shared-Memory Multiprocessor System	\$	Docket No.:	200302257-2

# PRELIMINARY AMENDMENT

**Mail Stop Patent Application  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450**

Date: September 26, 2003

Sir:

The Examiner is respectfully requested to enter the following amendment prior to the calculation of fees and the initial examination of the present continuation above-identified patent application under 1.53(b) in which the parent case 09/878,985 survives.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 8 of this paper.